

Notice of References Cited	Application/Control No. 10/824,504		Applicant(s)/Patent Under Reexamination MORITA ET AL.	
	Examiner Arpan P. Savla		Art Unit 2185	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,872,903 A	02-1999	Iwata et al.	713/400
*	B	US-6,226,753 B1	05-2001	Arima et al.	713/300
*	C	US-6,247,138 B1	06-2001	Tamura et al.	713/600
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Somaya, Deepak and Linden, Greg, "System-on-a-Chip Integration in the Semiconductor Industry: Industry Structure and Firm Strategies", December 10, 2000, CCC Working Paper No. 99-2, Haas School of Business, University of California at Berkeley. 20 June 2006 < http://ssrn.com/abstract=259676 >.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.